

FIG. 1
(Prior Art)

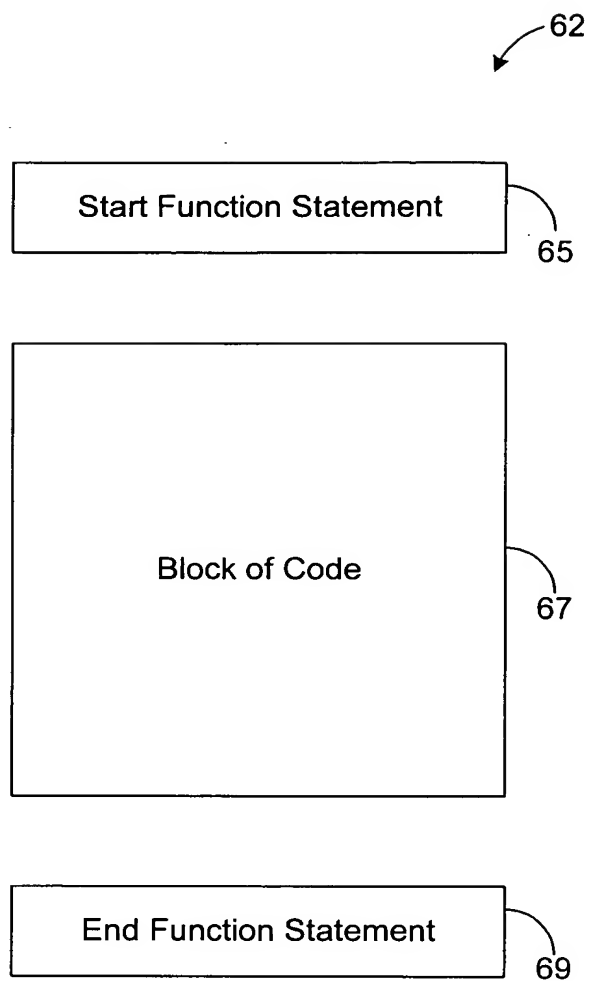


FIG. 2
(Prior Art)

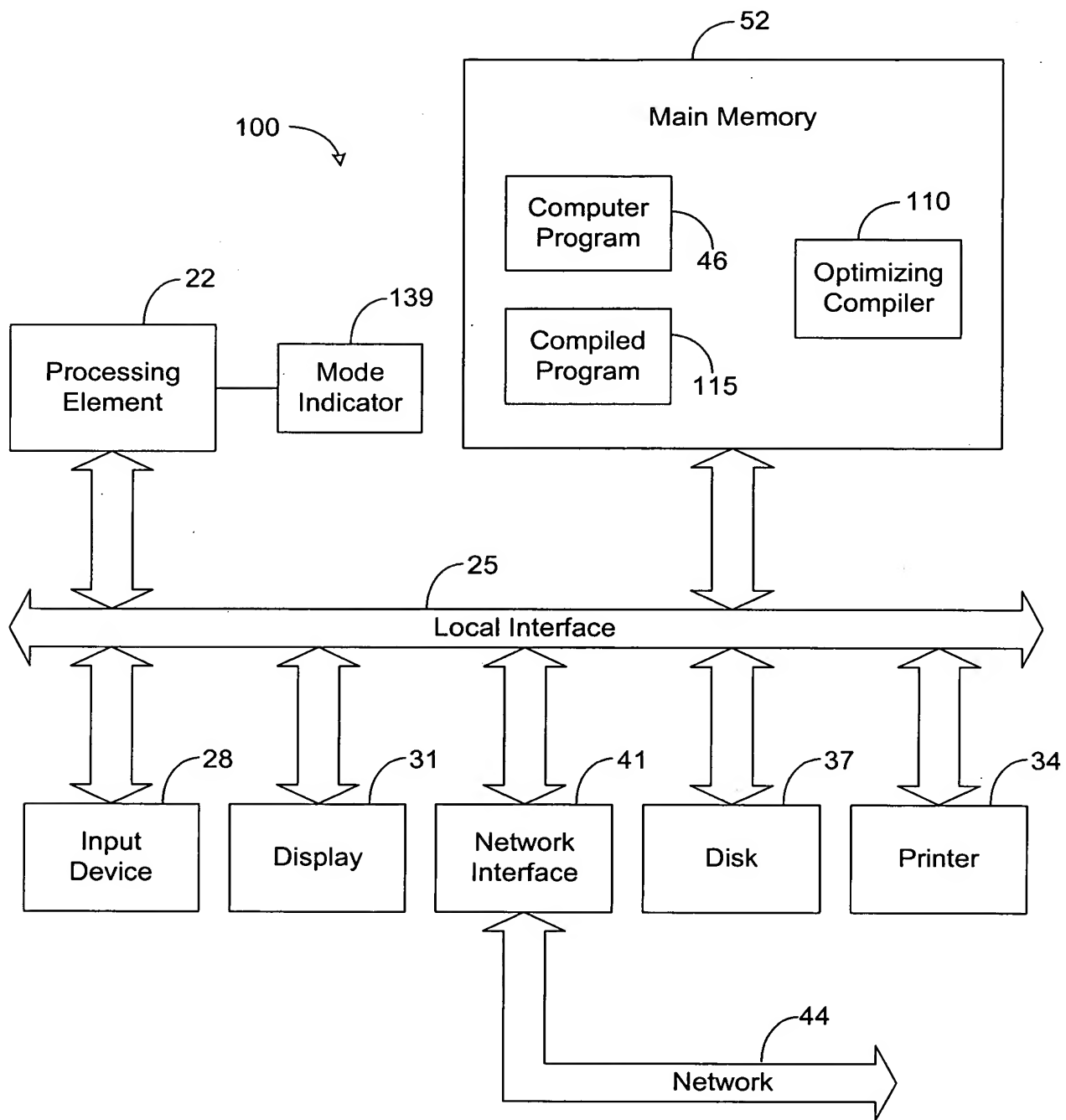


FIG. 3

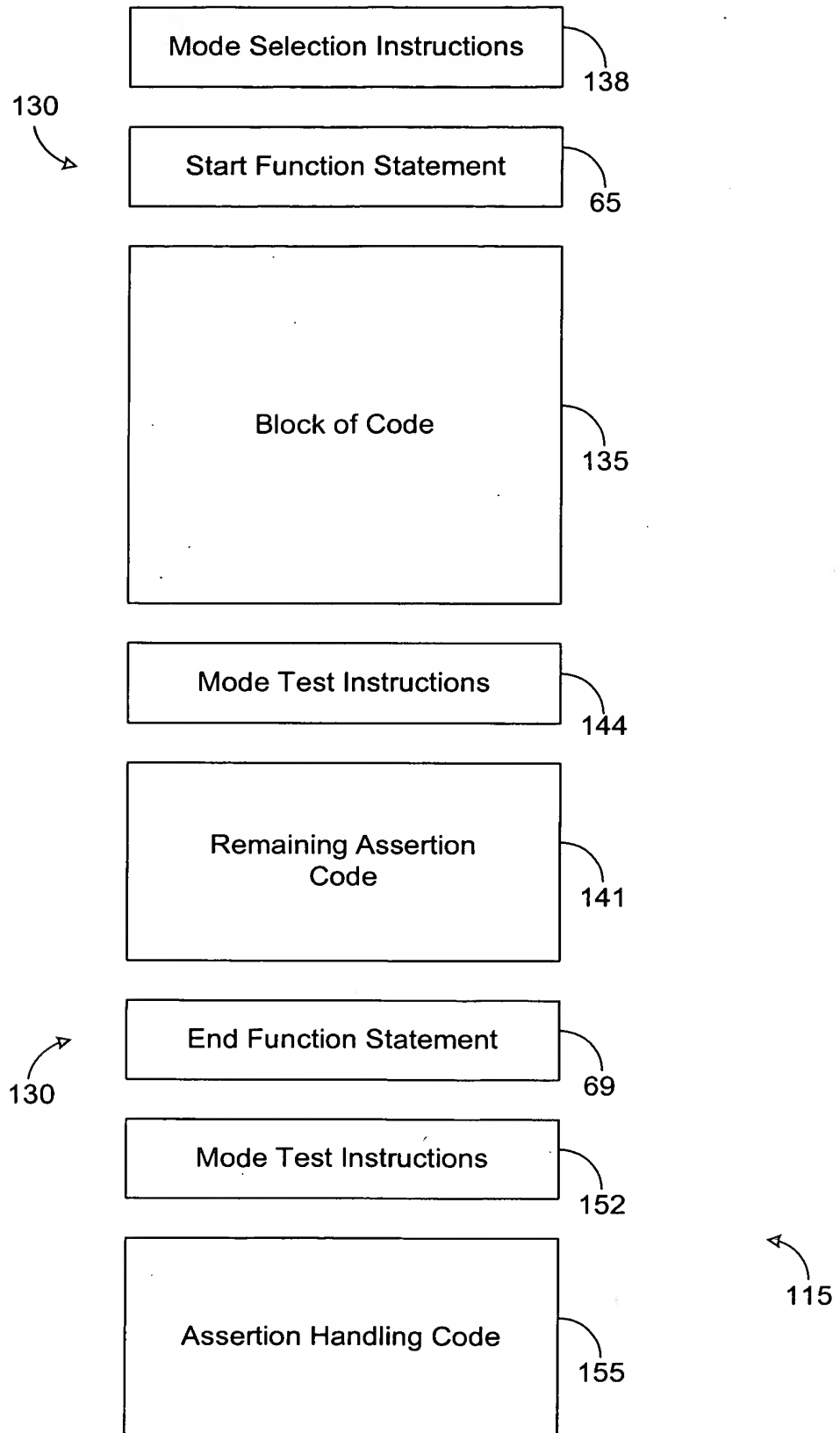


FIG. 4

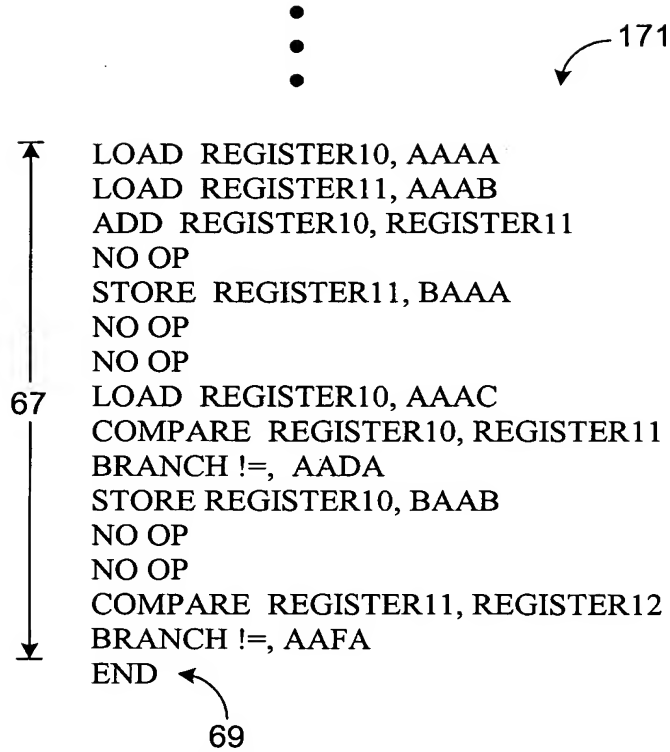


FIG. 5
(Prior Art)

182 → LOAD REGISTER1, NULL
LOAD REGISTER2, FFFA
COMPARE REGISTER1, REGISTER2
BRANCH !=, SKIP

184 → LOAD REGISTER3, NULL
LOAD REGISTER4, FFFB
COMPARE REGISTER3, REGISTER4
BRANCH !=, SKIP

186 → LOAD REGISTER5, NULL
LOAD REGISTER6, FFFC
COMPARE REGISTER5, REGISTER6
BRANCH !=, SKIP

FIG. 6

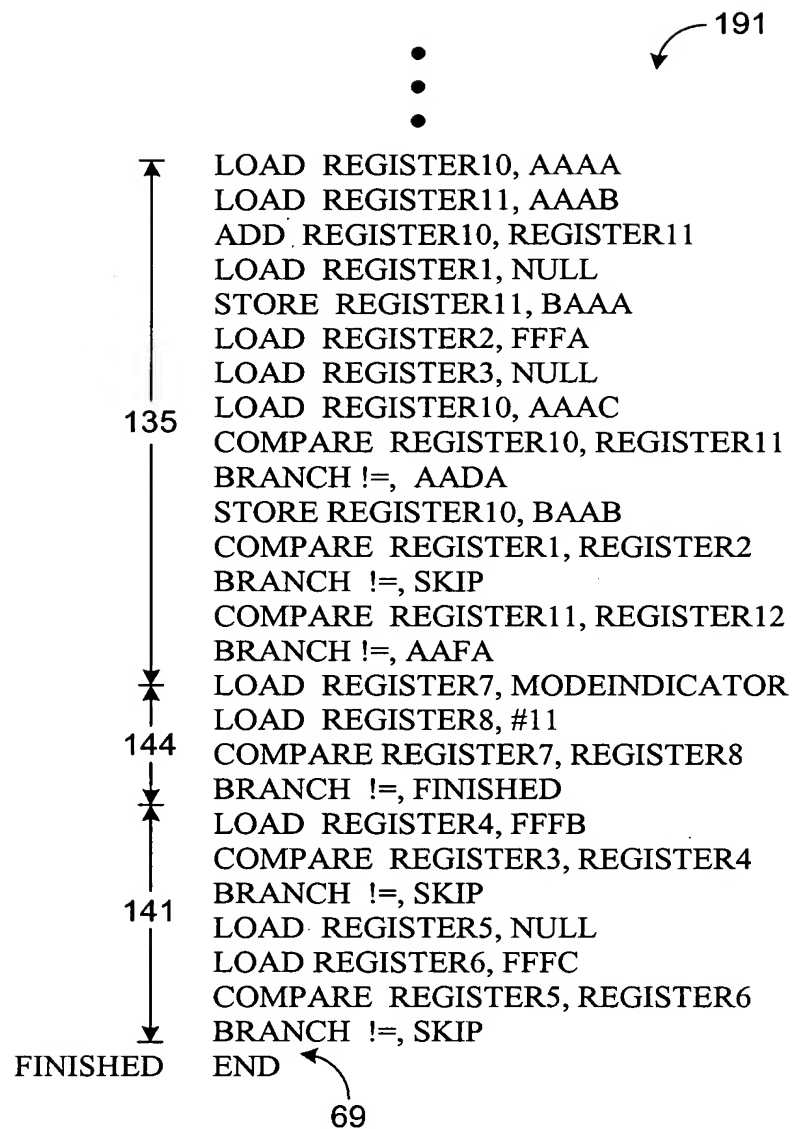


FIG. 7